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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/745,667	12/26/2000	Katsuyuki Matsumoto	001716	1951
7	7590 03/17/2003			
ARMSTRONG, WESTERMAN, HATTORI, McLELAND & NAUGHTON, LLP 1725 K Street, N.W., Suite 1000			EXAMINER	
			KASENGE, CHARLES R	
Washington, DC 20006			ART UNIT	PAPER NUMBER
•			2125	

DATE MAILED: 03/17/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)
		09/745,667	MATSUMOTO, KATSUYUKI
	Office Action Summary	Examiner	Art Unit
		Charles R Kaser	
Period fo	The MAILING DATE of this communication a or Reply	ppears on the cove	sheet with the correspondence address
THE N - Exter after: - If the - If NO - Failur - Any re	ORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION isions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reperiod for reply is specified above, the maximum statutory perion to reply within the set or extended period for reply will, by statutely received by the Office later than three months after the mailing dipatent term adjustment. See 37 CFR 1.704(b).	. 1.136(a). In no event, howe the statutory mind will apply and will expire the cause the application to	ever, may a reply be timely filed  imum of thirty (30) days will be considered timely.  SIX (6) MONTHS from the mailing date of this communication.
1)	Responsive to communication(s) filed on	·	
2a) <u></u>	This action is <b>FINAL</b> . 2b)⊠ T	his action is non-fi	nal.
3) <u></u> Dispositie		vance except for fo	rmal matters, prosecution as to the merits is
4)⊠	Claim(s) 1-5 is/are pending in the application	١.	
4	4a) Of the above claim(s) is/are withdra	awn from considera	ation.
5)	Claim(s) is/are allowed.		
6)⊠	Claim(s) <u>1-5</u> is/are rejected.		
7)	Claim(s) is/are objected to.		
	Claim(s) are subject to restriction and/ on Papers	or election requirer	ment.
9)[] T	he specification is objected to by the Examin	er.	
10)∐ T	he drawing(s) filed on is/are: a)□ acce	epted or b) objecte	ed to by the Examiner.
	Applicant may not request that any objection to the		
11) 🔲 T	he proposed drawing correction filed on		
	If approved, corrected drawings are required in re		
12) 🗌 T	he oath or declaration is objected to by the E	xaminer.	
Priority u	nder 35 U.S.C. §§ 119 and 120		
13)🛛 🛚	Acknowledgment is made of a claim for foreig	n priority under 35	U.S.C. § 119(a)-(d) or (f).
	☑ All b) ☐ Some * c) ☐ None of:		
	1.⊠ Certified copies of the priority documen	ts have been recei	ved.
2	2. Certified copies of the priority documen	ts have been recei	ved in Application No.
	3. Copies of the certified copies of the price application from the International Buste the attached detailed Office action for a list	ority documents ha	ve been received in this National Stage 7.2(a)).
			U.S.C. § 119(e) (to a provisional application).
a)	☐ The translation of the foreign language pr	ovisional applicatio	n has been received.
15)∐ At !\ttachment	cknowledgment is made of a claim for domes	tic priority under 35	U.S.C. §§ 120 and/or 121.
Notice Notice Notice	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲	nterview Summary (PTO-413) Paper No(s) Notice of Informal Patent Application (PTO-152) Other:
Patent and Trac O-326 (Rev.		ction Summary	Part of Paper No. 4

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#### **DETAILED ACTION**

### Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1-3 are rejected under 35 U.S.C. 102(e) as being anticipated by Shimazaki U.S. Patent 6,338,143. Shimazaki discloses a portable electronic device (col. 2, lines 43-45) comprising a power source terminal for connecting to an external power source, an internal power source which is lower than the external power source in voltage (col. 2, lines 1-10), first circuit means to execute first data processing, and second circuit means to execute second data processing which has lower speed than the first data processing (col. 4, lines 31-52), the electronic device being characterized in that the device is so constructed that the external power source supplies power to the device through a power source terminal when the first circuit means

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is in an active mode, and the internal power source supplies power to the device when the second circuit means is in the active mode (col. 2, lines 1-10).

Shimazaki discloses the internal power source being a dry battery or a secondary battery (col. 1, lines 65-67), the portable electronic device having a USB connector conforming to the Universal Serial Bus standard, and one terminal of the USB connector provides the power source terminal (col. 3, lines 19-22).

Shimazaki discloses the first circuit means is provided by a data communication controller to execute predetermined data communication processing for performing data communication with an external information processing device (col. 3, lines 19-27 and col. 4, lines 8-10), and the second circuit means is provided by a control circuit to execute device operation processing for usual operation of the device (col. 6, lines 31-34).

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Shimazaki as applied to claim 1 above, and further in view of Morgan U.S. Patent 5,764,502. Shimazaki discloses interconnected power supplying wires extending from external and internal power sources (Fig. 3). The office also interprets power-supplying wires as inherent to a portable electronic device having internal and external power sources. Shimazaki does not expressly



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disclose using a reverse flow resist means for two power supplying wires. Morgan discloses power supplying wires extending from a power source and having a reverse flow resist means (col. 15, lines 13-37).

At the time this invention was made, it would have been obvious to a person of ordinary skill in the art to use Morgan's reverse flow resist means for Shimazaki's power supplying wires. One of ordinary skill in the art would have been motivated to do this since it is commonly known to use a reverse flow resist means for a power supplying wire in order to protect from overloading the power source.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Charles R Kasenge whose telephone number is 703 305-8592. The examiner can normally be reached on Monday through Friday, 8:30 - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Leo Picard can be reached on 703 308-0538. The fax phone numbers for the organization where this application or proceeding is assigned are 703 746-7239 for regular communications and 703 746-7239 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 308-0538.

L. P. Pund

CK March 12, 2003

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100